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Express Mail #EL157416754US

Asst. Commissioner for Patents

Washington, DC 20231

OFGS File No. : P/1071-1435

Inventor

: Yasuhiro TANAKA, et al.

Title

: CENTER-ELECTRODE ASSEMBLY AND MANUFACTURING

METHOD THEREFOR, NONRECIPROCAL CIRCUIT DEVICE

AND COMMUNICATION APPARATUS USING THE SAME

Assignee

: Murata Manufacturing Co., LTD.

Enclosed herewith please find the following documents in the above-identified application for United States Letters Patent:

	Print EFS Data Sheet
<u>17</u>	Pages of Specification including Abstract and Claims
5	Numbered Claims Calculated as 5 Claims for Fee Durnoges
10	Sheets of Drawing Containing Figures 1 to 14. (Formal/Informal)
<u>X</u>	Declaration and Power of Attorney
X	Priority is Claimed under 35 U.S.C. §119:
Conver	ntion Date August 25, 2000 for Japanese Appln. S.N. 2000-256434
<u>X</u>	Certified Priority Application
/X	Assignment
<u> X</u>	Return-Addressed Post Card
OFGS C	Check No. 06044, which includes the fee of \$750.00, calculated as
follow	vs:
	Basic Filing Fee:
	Additional Filing Fees:
	Total Number of Claims in Excess of 20, times \$18:
	Number of Independent Claims in Excess of 3, times \$80.
	One or More Multiple Dependent Claims: Total \$270
	Total Filing Fees or
	Total Filing Fee Reduced 50% for Small Entity:
	Assignment Recording Fee: \$40 \$ 40.00
	TOTAL Filing Fee and Assignment Recording Fee:
	$\frac{5}{5}$

In the event the actual fee is greater than the payment submitted or is inadvertently not enclosed, or if any additional fee during the prosecution of this case is not paid, the Patent and Trademark Office is authorized to charge the underpayment to Deposit Account No. 15-0700.

EXPRESS MAIL CERTIFICATE

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail Post Office to Addressee (mail label #EL157416224US) in an envelope addressed to: Asst. Commissioner for Patents, Washington, D.C. 20231, on August 15, 2001.

Dorothy Jenkins

Name of Person Mailing Correspondence

Signature August 16, 2001

Date of Signature

Respectfully submitted,

James A. Finder

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CERTIFICATION

I am the individual who sealed the envelope addressed to the Commissioner of Patents and Trademarks for the new application indicated below.

I certify that immediately before sealing the envelope, I checked its contents and made sure that each and every item listed on the transmittal letter was in the envelope. I then sealed the envelope.

I declare that all statements made herein of my own knowledge are true and were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of title 18 of the United State Code and that such willful statements may jeopardize the validity of the application of any patent issued thereon.

Dated: August 16, 2001

OFGS File Number: P/1071-1435

First Applicant : Yasuhuro Tanaka

Title : CENTER-ELECTRODE ASSEMBLY AND

MANUFACTURING METHOD THEREFOR, NONRECIPROCAL CIRCUIT DEVICE AND

COMMUNICATION APPARATUS USING THE SAME

Grace M. Espinal